

MCA 102 COMPUTER ORGANIZATIONS AND ARCHITECTURE

Module 1 (10 Hours)

Introduction: Basic architecture of computer, Functional units, Operational concepts, Bus structures, Von Neumann Concept.

Basic Processing: Instruction code, Instruction set, Instruction sequencing, Instruction Cycle & Execution Cycle, Instruction format, Addressing modes, Micro instruction, Data path and control path design, Micro programmed vs. Hardwired controlled unit, RISC vs. CISC.

Arithmetic: Design of ALU, Binary arithmetic, Addition and Subtraction of signed number, Multiplication of Positive number, Signed operand multiplication, Division, Floating point number representation and arithmetic.

Digital Electronics: Boolean algebra, Digital Logic, Truth Tables, K map, Number system, Flip - Flop

Module 2 (10 Hours)

Memory: Memory Hierarchy, RAM, ROM, Cache memory organization, Mapping techniques, Virtual memory, Memory Interleaving, Secondary Storage, Flash drives.

Module 3(10 Hours)

Input/output: Accessing I/O devices, I/O mapped I/O, Programmed I/O, Memory Mapped I/O, Interrupt Driven I/O, Standard I/O interfaces, Synchronous and Asynchronous Data transfer, DMA data transfer.

Introduction to Parallel processing: Flynn's Classification, Pipelining, Super Scalar processors, Array processing, vector processing.

Module 4 (10 Hours)

8085 Microprocessor and Assembly level Programming using 8085 microprocessor

Module 5 (6 hours)(as per choice of faculty)

Portion covered can be tested through Internal evaluation only not to be included in University examination)

Text Books:

1. Mano.M. "Computer System and Architecture" (3rd Ed) (PHI).
2. Computer Architecture by Hwang and Briggs. (MGH).
3. Fundamentals of Computer Organisation by M V L N Raja Rao; Scitech publ.
4. Carl Hamacher, ZvonkoVranesic, SafwatZaky, "ComputerOrganization", 5th Edition, McGraw-Hill Education India

Reference Books:

1. William Stalling, "Computer Organization and Architecture", Pearson Education
2. J. P. Hayes, "Computer Architecture and Organization", MGH
3. A.S. Tananbaum, "Structured Computer Organization", Pearson Education